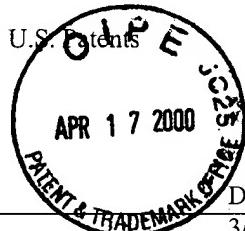


FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 36856.00260	SERIAL NO.: 09/501,084
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT(S): Yasuyuki NAITO, et al.	
	FILING DATE: February 9, 2000	GROUP:

Reference Designation



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TECHNOLOGY CENTER 2800

Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
	AA 3,308,359	3/1967	Hayworth, et al.		
	AB 3,612,963	10/1971	Piper, et al.		
	AC 3,822,397	7/1974	Puppolo, et al.		
	AD 3,971,970	7/1976	Voyles, et al.		
	AE 4,074,340	2/1978	Leigh		
	AF 4,274,124	6/1981	Feinberg, et al.		
	AG 4,295,183	10/1981	Miersch, et al.		
	AH 4,328,530	5/1982	Bajorek, et al.		
	AI 4,346,429	8/1982	DeMatos		
	AJ 4,419,714	12/1983	Locke		
	AK 4,424,552	01/03/84	R. Saint Marcoux		

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	English Translation?
AL 6-260364	9/16/94	Japan	—	Yes
AM 0 191 668	08/20/86	EPO	—	Yes
AN				
AO				
AP				
AQ				
AR				
AS				
AT				

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AU A paper entitled "Technical Information: Low Inductance Capacitors for Digital Circuits," by John Galvagni of AVX Corporation, Myrtle Beach, SC (No date provided.) NOT considered, no date

AV A paper entitled "Technical Information: Advanced Decoupling Using Ceramic MLC Capacitors," by John D. Prymak of AVX Corporation, Olean, NY (No date provided.) NOT considered, no date

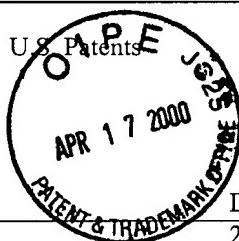
EXAMINER *L Anthony Dunkum*

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT(S): Yasuyuki NAITO, et al.	
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Subclass if
appropriate

Examiner Initial	Document Number	Date	Name	Class
AA	AA 4,430,690	2/1984	Chance, et al.	/
AB	AB 4,706,162	11/1987	Hernandez, et al.	/
AC	AC 4,814,940	3/1989	Horstmann, et al.	/
AD	AD 4,830,723	5/1989	Galvagni, et al.	/
AE	AE 4,831,494	5/1989	Arnold, et al.	/
AF	AF 4,852,227	8/1989	Burks	/
AG	AG 4,853,826	8/1989	Hernandez	/
AH	AH 4,862,318	8/1989	Galvagni, et al.	/
AI	AI 5,517,385	5/1996	Galvagni, et al.	/
AJ	AJ 5,880,925	3/1999	DuPre, et al.	/

FOREIGN PATENT DOCUMENTS

English
Translation?

Document Number	Date	Country	Class
AK			
AL			
AM			
AN			
AO			
AP			
AQ			
AR			
AS			
AT			

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AU 1991 Symposium on VLSI Technology, Digest of Technical Papers entitled " Multilayer Vertical Stacked Capacitors (MVDTC) for 64 Mbit and 256 Mbit DRAMS by D. Temmler, Institute of Semiconductor Physics, Germany

AV IBM Technical Disclosure Bulletin (vol.31 no. 3 August 1988)

AW IBM Technical Disclosure Bulletin (vol.32 no. 6B November 1989)

EXAMINER

DATE CONSIDERED 3/10/01

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT(S): Yasuyuki NAITO et al.	
	FILING DATE: February 9, 2000	GROUP: 2831

Reference Designation

U.S. Patents

Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
AA					
AB					
AC					
AD					
AE					
AF					
		FOREIGN PATENT DOCUMENTS			
	Document Number	Date	Country	Class	Filing Date Subclass if appropriate
	AG 2-256216 (w/ English abstract)	10/1990	Japan	—	
	AH 8-172026 (w/ English abstract)	7/1996	Japan	—	
	AI 6-140283 (w/ English abstract)	5/1994	Japan	—	
	AJ 2 707 123	6/1993	France	—	
	AK 25 45 672	4/1977	Germany	—	
	AL 1,464,631	11/1965	France	—	
	AM 4-42910	2/1992	Japan	—	
	AN				
	A0				
	OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)				
	AP 40th Electronic Components & Technology Conference, 1990 Poster Presentations, Volume 2, May 20-23, 1990, pages 1014-1023.				
	AR				
	AS				
	AT				
EXAMINER	DATE CONSIDERED				3/14/01

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